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XDSL MODEM

State of the art

Fig. 1

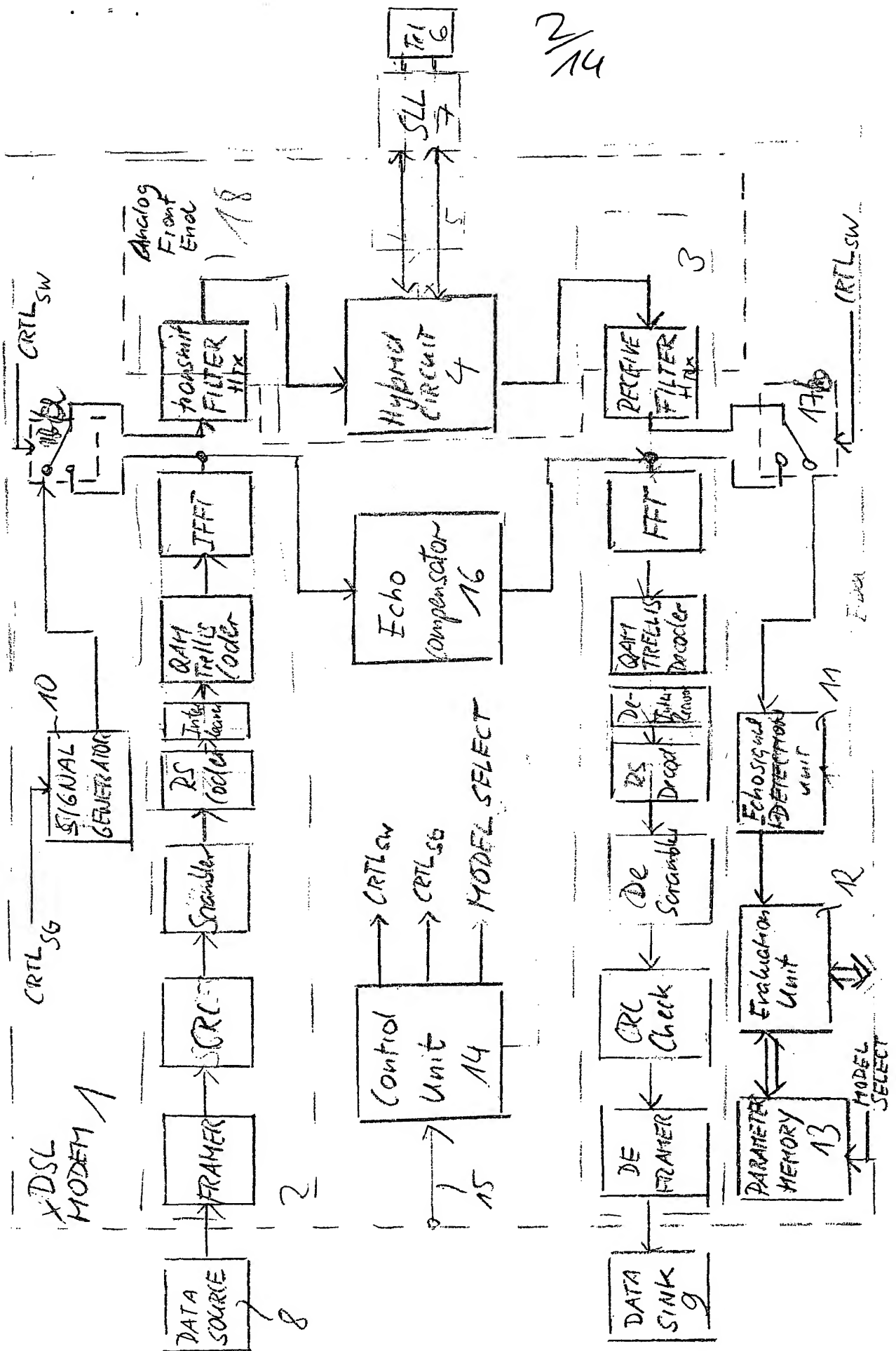


Fig. 2

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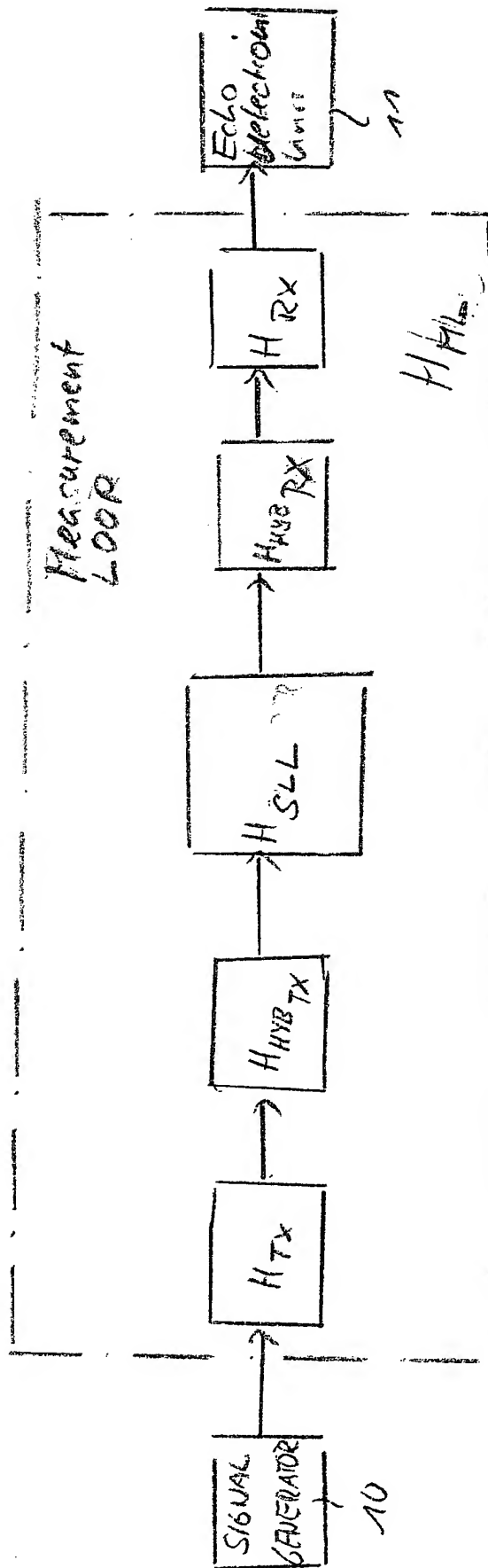


Fig 3

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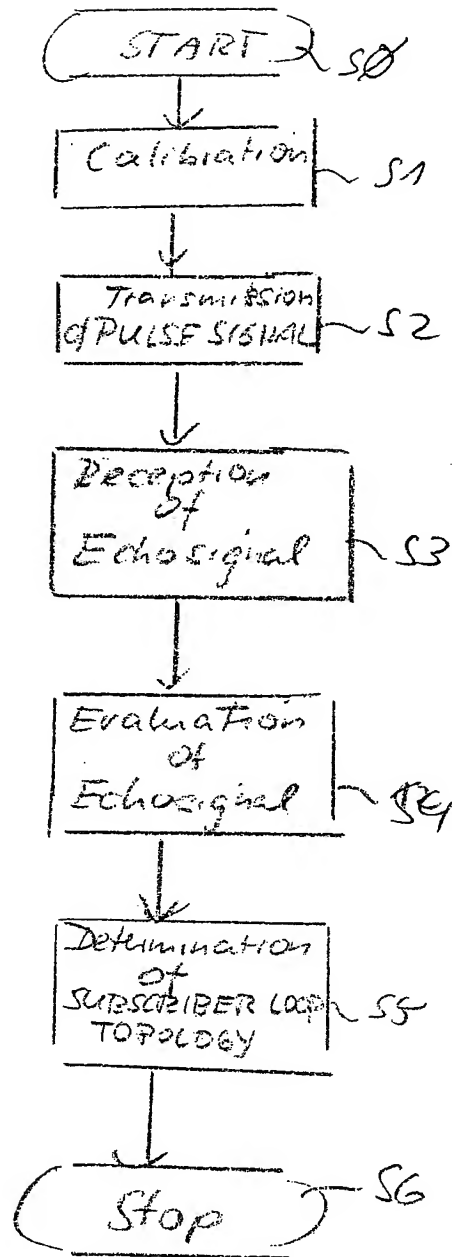


Fig. 4

$\frac{J}{14}$

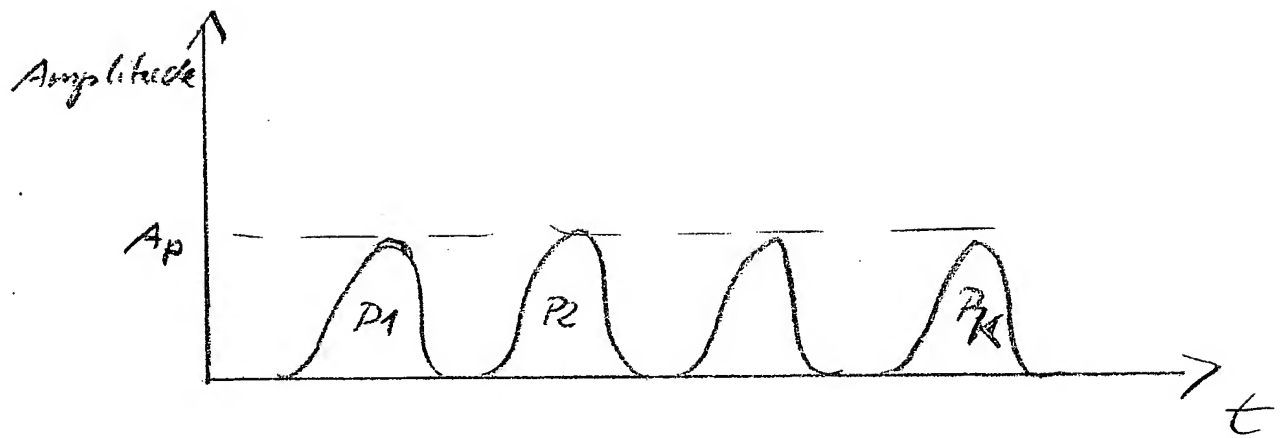


Fig. 5

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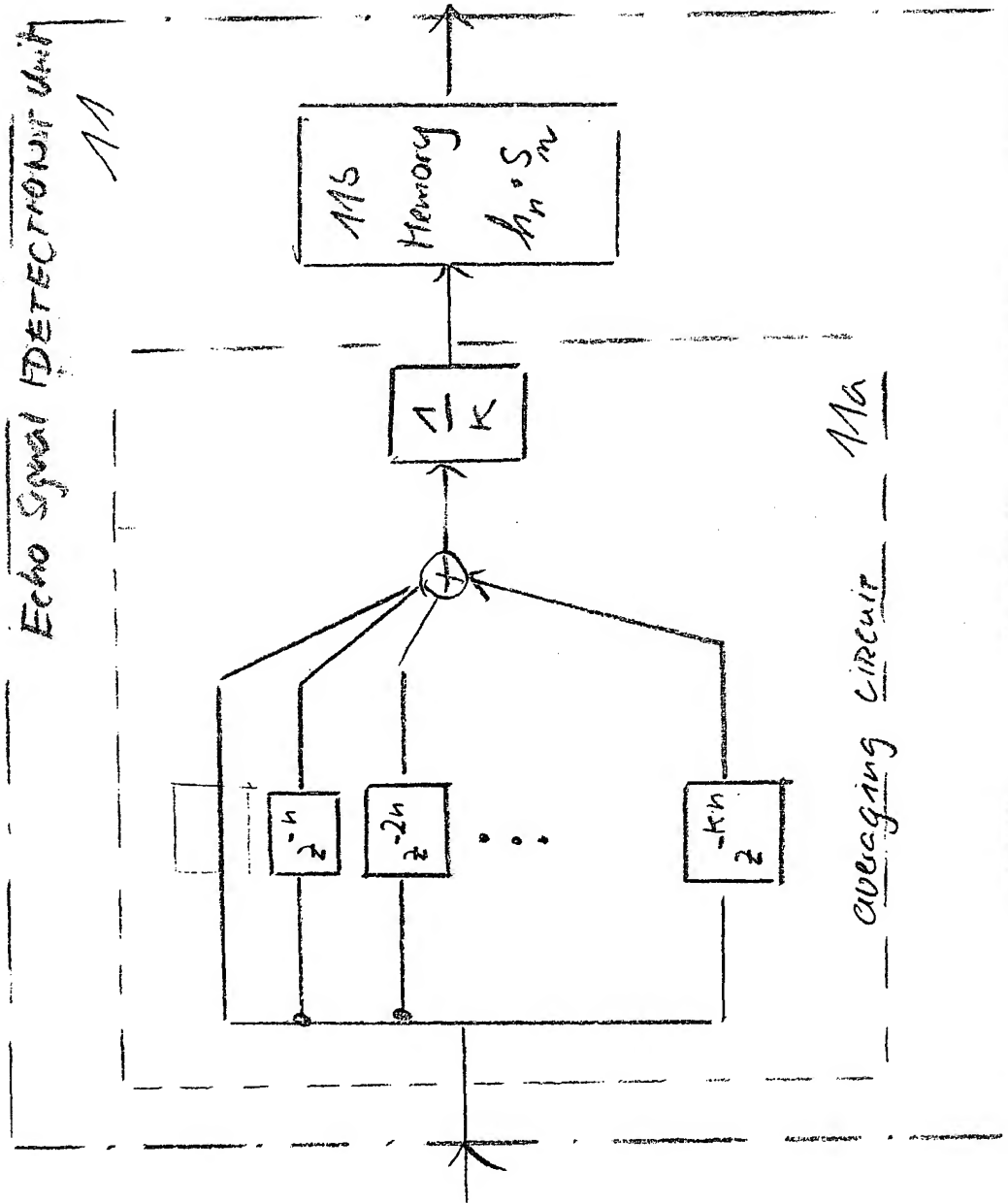


Fig. 6

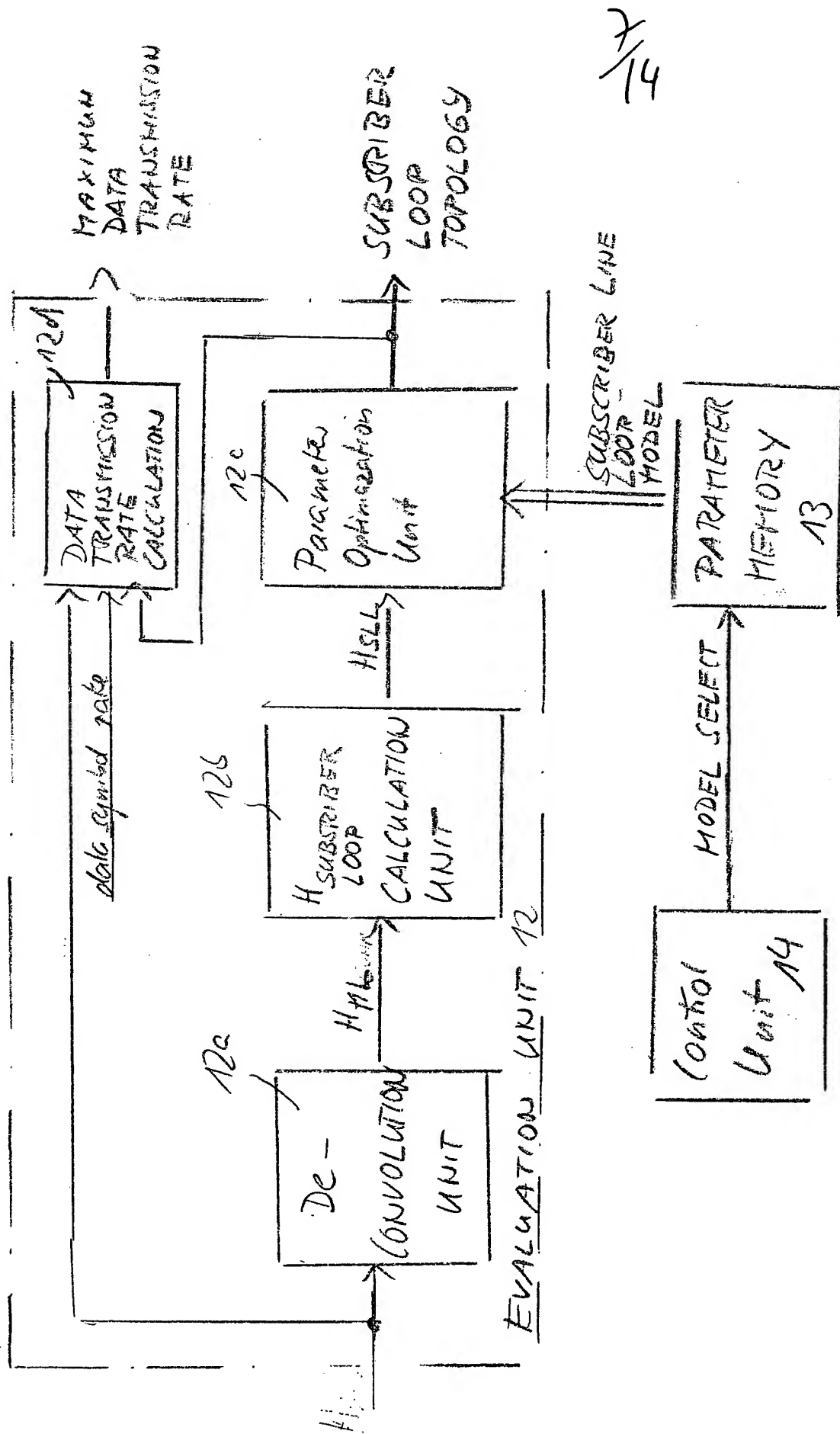


Fig 7

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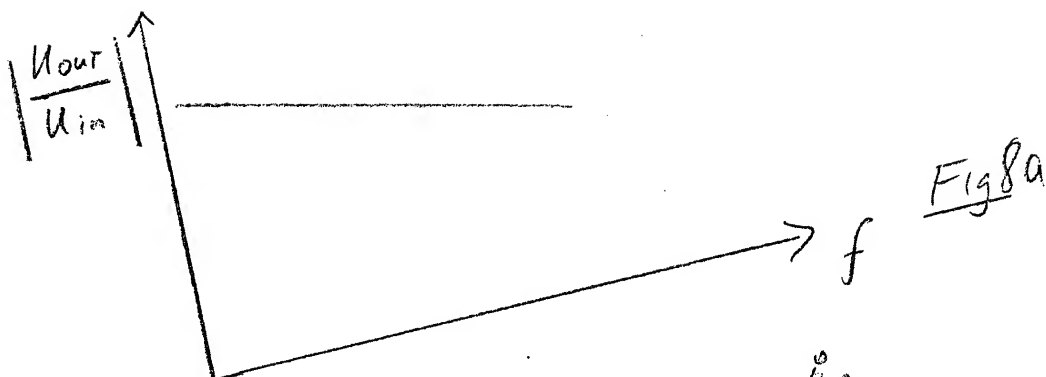


Fig 8a

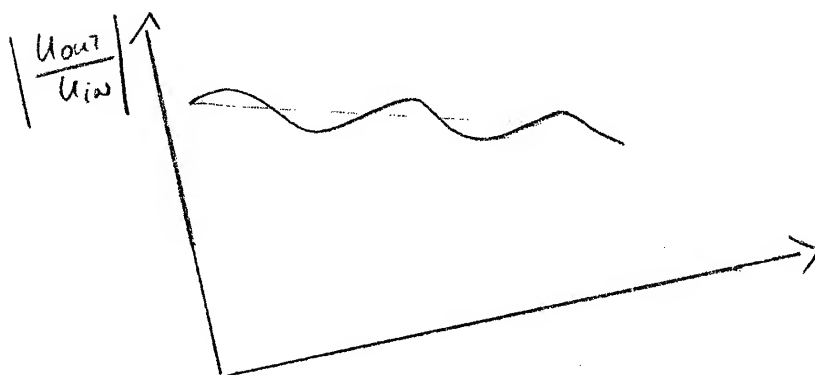
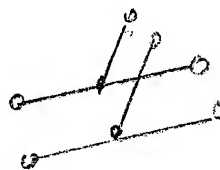


Fig 8b



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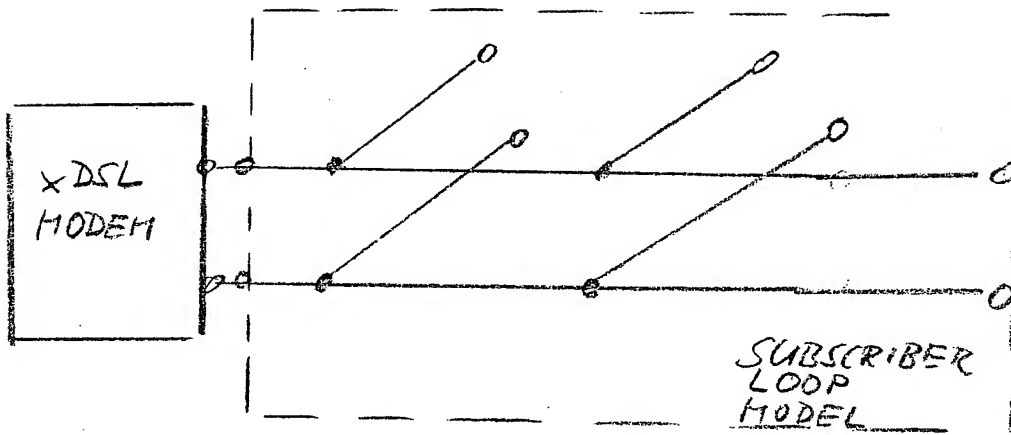


Fig 9a

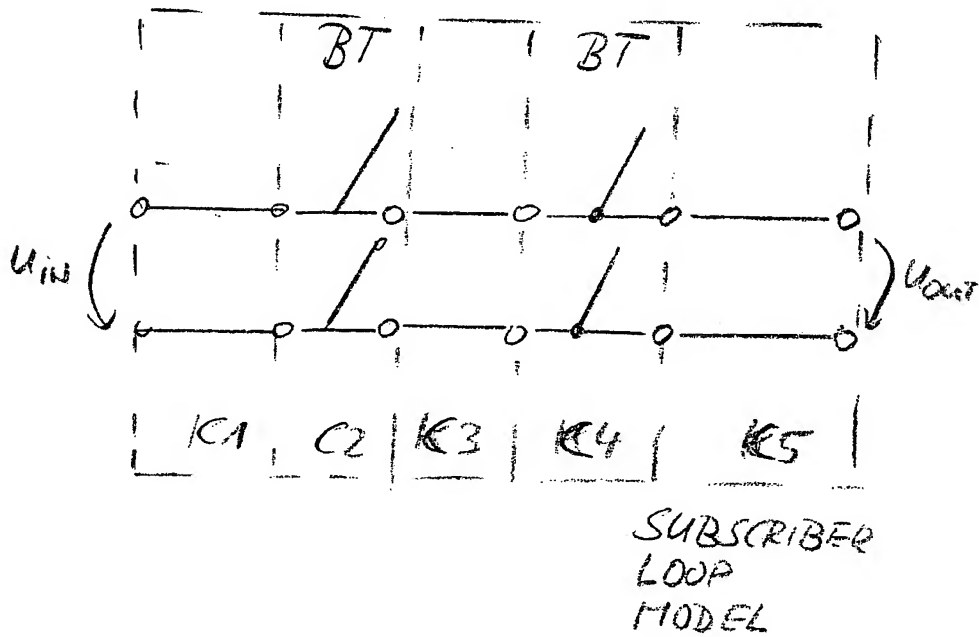


Fig 9b

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### SUBSCRIBER LOOP TOPOLOGY

Cable	Cable length	Cable Diameter	Cable TYPE
$C_1$	$l_1$	$\phi_1$	$T_1$
$C_2$	$l_2$	$\phi_2$	$T_2$
$C_3$	$l_3$	$\phi_3$	$T_3$
$\vdots$	$\vdots$	$\vdots$	$\vdots$
$\vdots$	$\vdots$	$\vdots$	$\vdots$
$\vdots$	$\vdots$	$\vdots$	$\vdots$
$C_N$	$l_N$	$\phi_N$	$T_N$

Fig. 10

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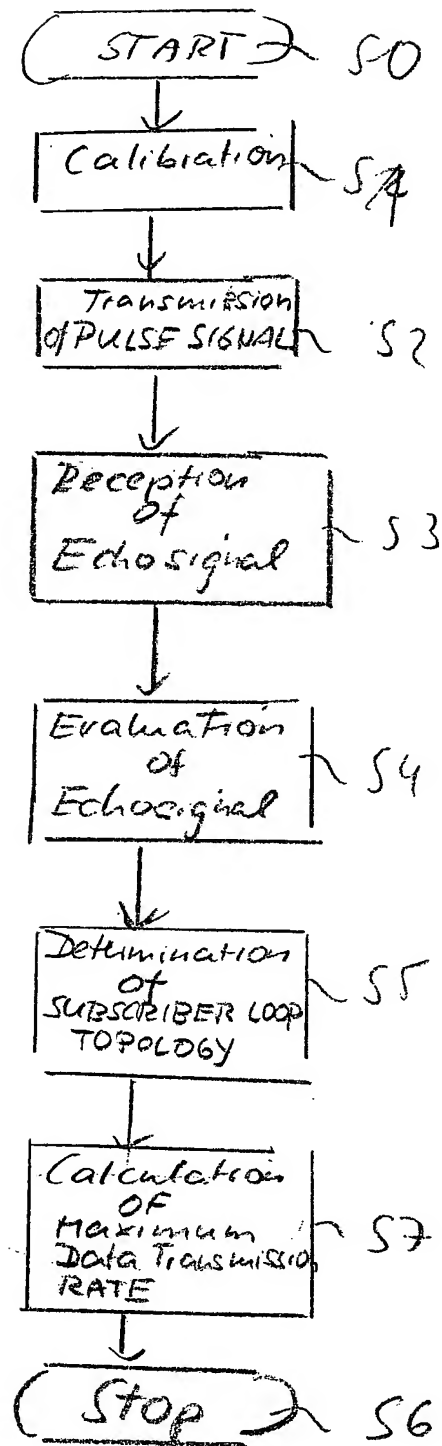


Fig. 11

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DATA  
TRANSMISSION  
RATE  
CALCULATION

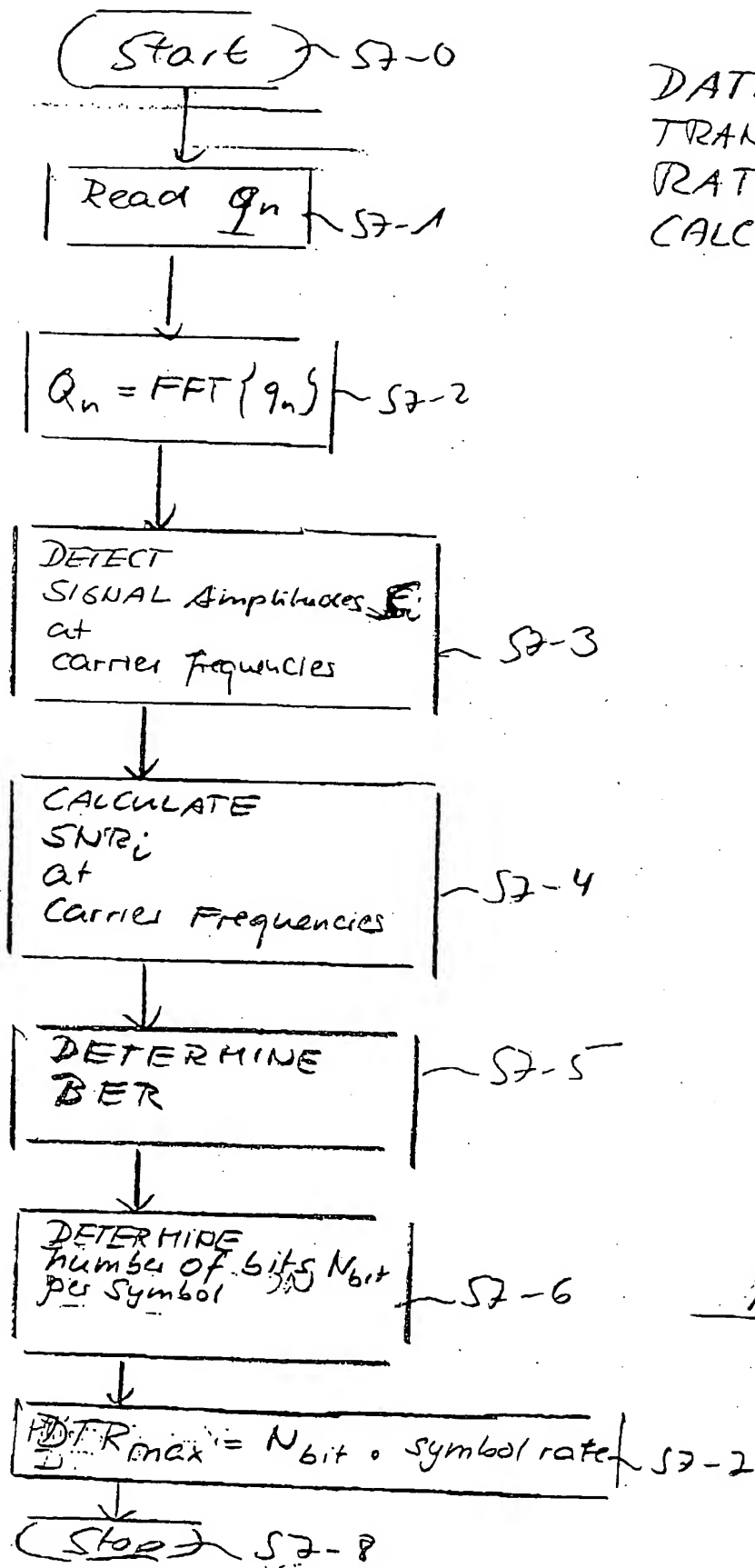


Fig 12

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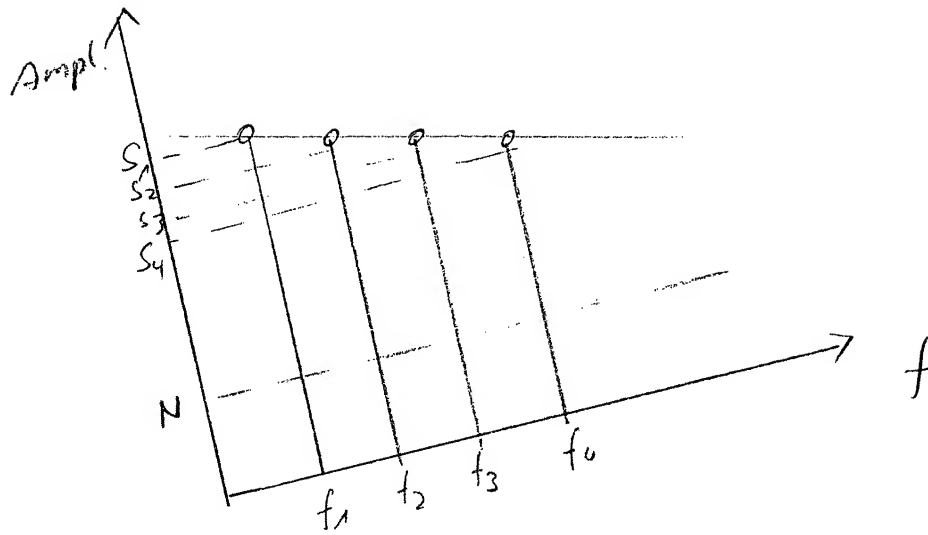


Fig 13a

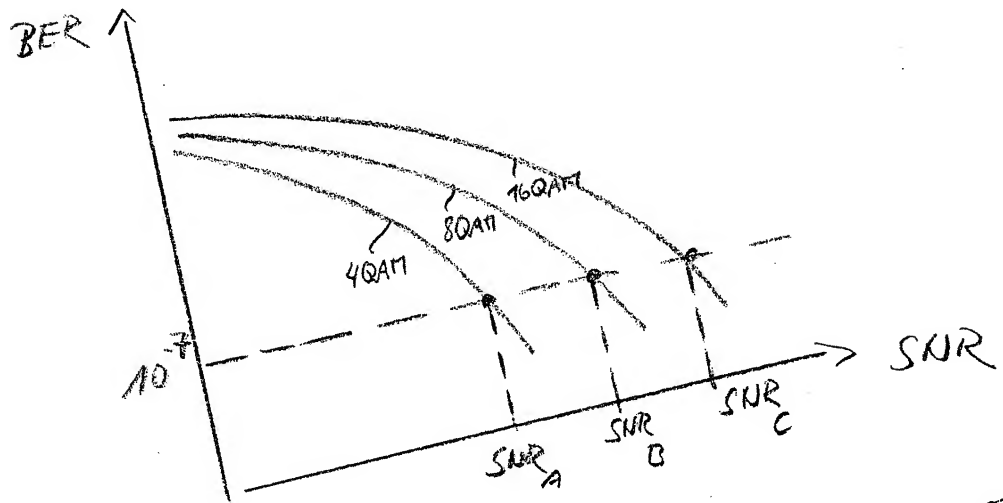
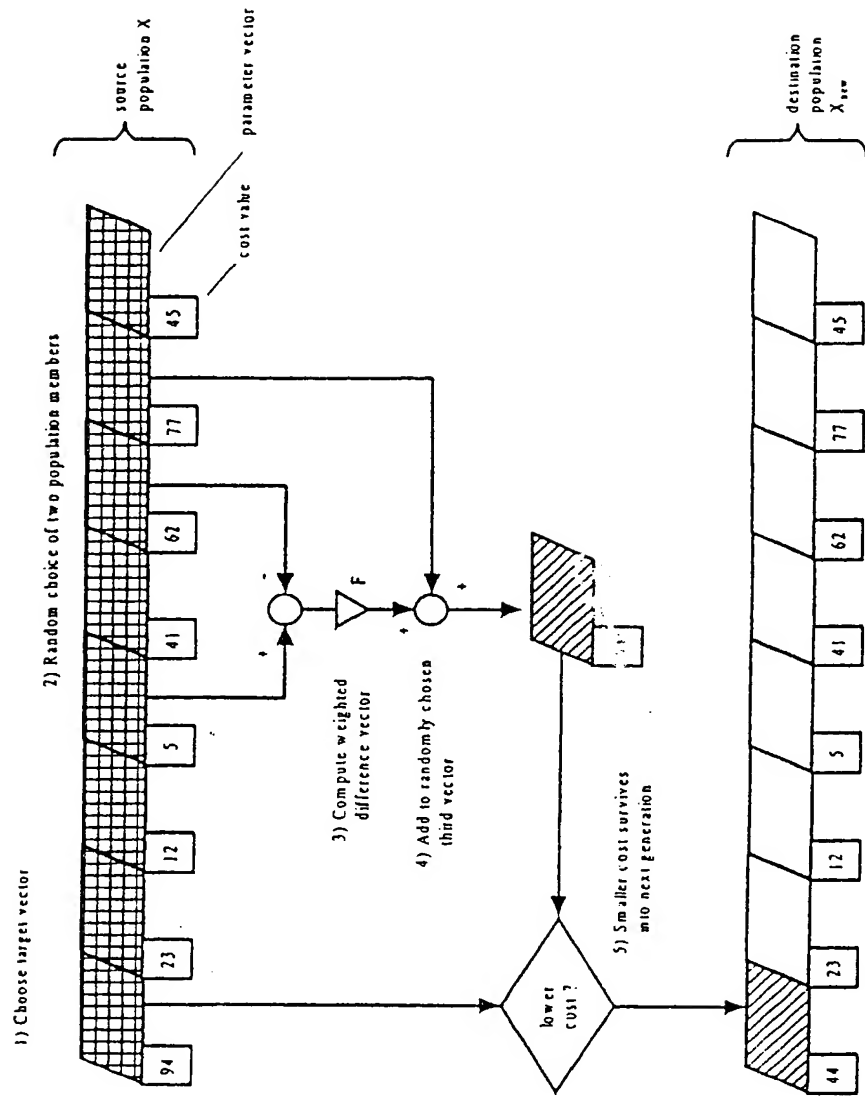


Fig 13b

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population arrays swap places for next generation

Figure 14